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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re PATENT APPLICATION of

Jun Kanamori

Group Art Unit: 2814

Serial No.: 09/398,189

Examiner: S. Rao

Filed: September 17, 1999

Confirm. No.: 9755

For:

METHOD OF FABRICATING A SEMICONDUCTOR DEVICE WITH SELF-

ALIGNED SILICIDE AREAS FORMED USING A SUPPLEMENTAL SILICON

**OVERLAY** 

## **AMENDMENT UNDER 37 C.F.R. 1.116**

U.S. Patent and Trademark Office 2011 South Clark Place Customer Window, **Mail Stop AF** Crystal Plaza Two, Lobby, Room 1B03 Arlington, VA 22202

Date: March 1, 2004

(Monday)

Sir:

In response to the Final Office Action dated September 29, 2003, the period for response having been extended two (2) months to February 29, 2004, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Serial No. 09/398,189 IIZ.008D Amendment dated March 1, 2004

Remarks/Arguments begin on page 9 of this paper.